



V-RAN Engineer

Amarisoft is a highly technical and fully independent company. Funded in 2012, Amarisoft develops a full software stack for LTE wireless communication (eNodeB, EPC and UE simulator) that is used by more than 200 customers over the world in many markets (R&D, research, PMR, Telco, UE vendors...).

Our technology is based on high level of performance and optimization, which allows us to prove what was considered impossible in Telecom industry: Delivering state-of-the-art LTE software stack including eNodeB, EPC and UE running live on x86 CPU (ASIC-less and DSP-less). We are delighted to bring some affordable tools and software to the 4G mobile community to unleash creativity and at the end expand communications among people. Accessible technology is the basement of possible success story. We at Amarisoft are working on helping all companies and individuals to be a player in mobile networks of now and future generation.

To enforce our V-RAN team, we are looking for a highly talented C developer found of optimization, real time systems and scalable architecture.

This position takes place in the challenging and new domain of Radio Access Network virtualization.

You will have to push our code to the limit of intel x86 CPU power by optimizing code and algorithm including SIMD coding, mathematical optimization, cache management and much more.

You will also participate in making our architecture highly scalable to run it on massive core/CPU servers reaching real time constraints.

You will be part of our expert development team in a stimulating and open environment.

With highly competitive salaries, Amarisoft is widely considered as a disruptive company in Telecom industry. If you're a creative and autonomous engineer with a genuine passion for technology, we want to hear from you.

Key responsibilities:

- In charge of code optimizations, including mathematical computing and advanced algorithms.
- In charge of stack scalability involving all aspects of our technology.
- You will follow CPU work evolution and lead hardware definition of all of our product.

Key requirements:

- A good degree from a leading university in an engineering or computer science related discipline. PhD preferred.
- Strong knowledge of CPU operation, including SIMD, cache management. Intel architecture knowledge would be perfect.
- Experience of multi-thread systems on multi core architecture.
- Knowledge of profiling tools and methods.
- Ability to read assembly code.
- Creative, autonomous and able to work with a minimum of supervision.
- experience in wireless domain is a plus.

Location: Paris, France

To apply, please contact hr@amarisoft.com